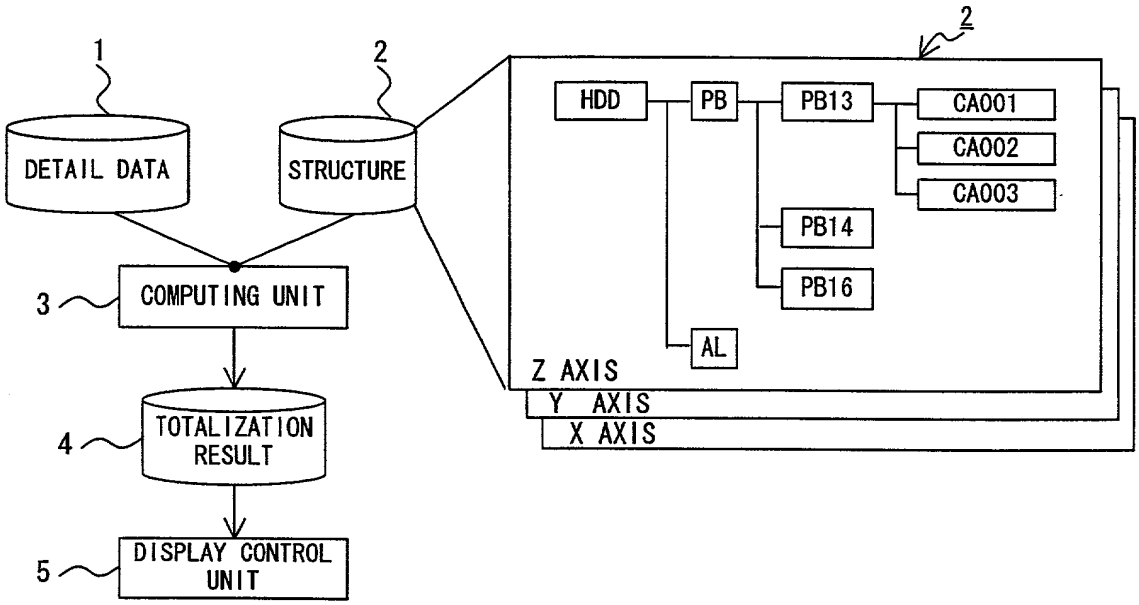

1
2
DETAIL DATA
STRUCTURE
2
HDD
PB
PB13
CA001
CA002
CA003
PB14
PB16
AL
Z AXIS
Y AXIS
X AXIS
3
COMPUTING UNIT
4
TOTALIZATION RESULT
5
DISPLAY CONTROL UNIT

F I G. 1

JP 2020-155060 A

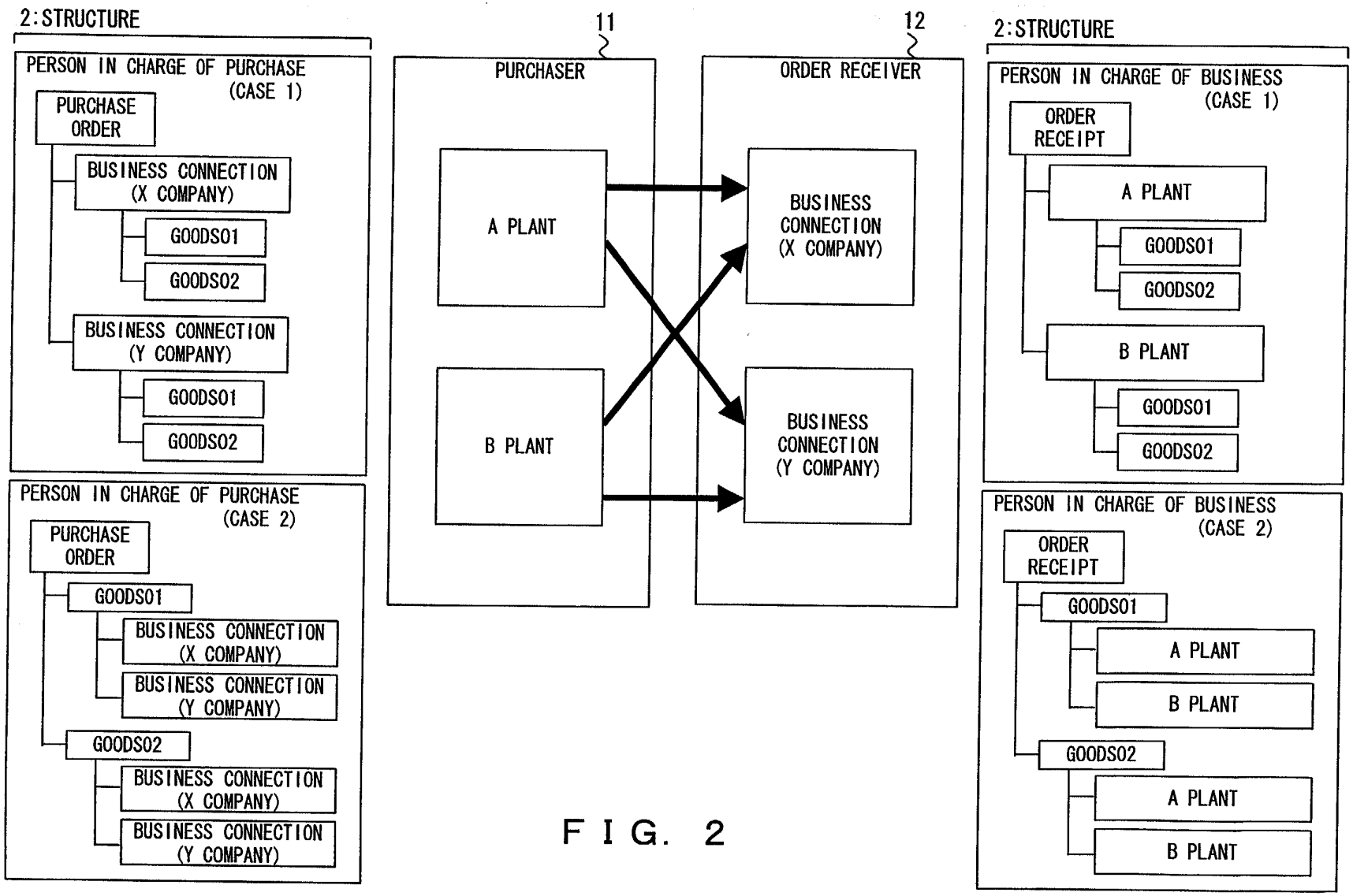



FIG. 2

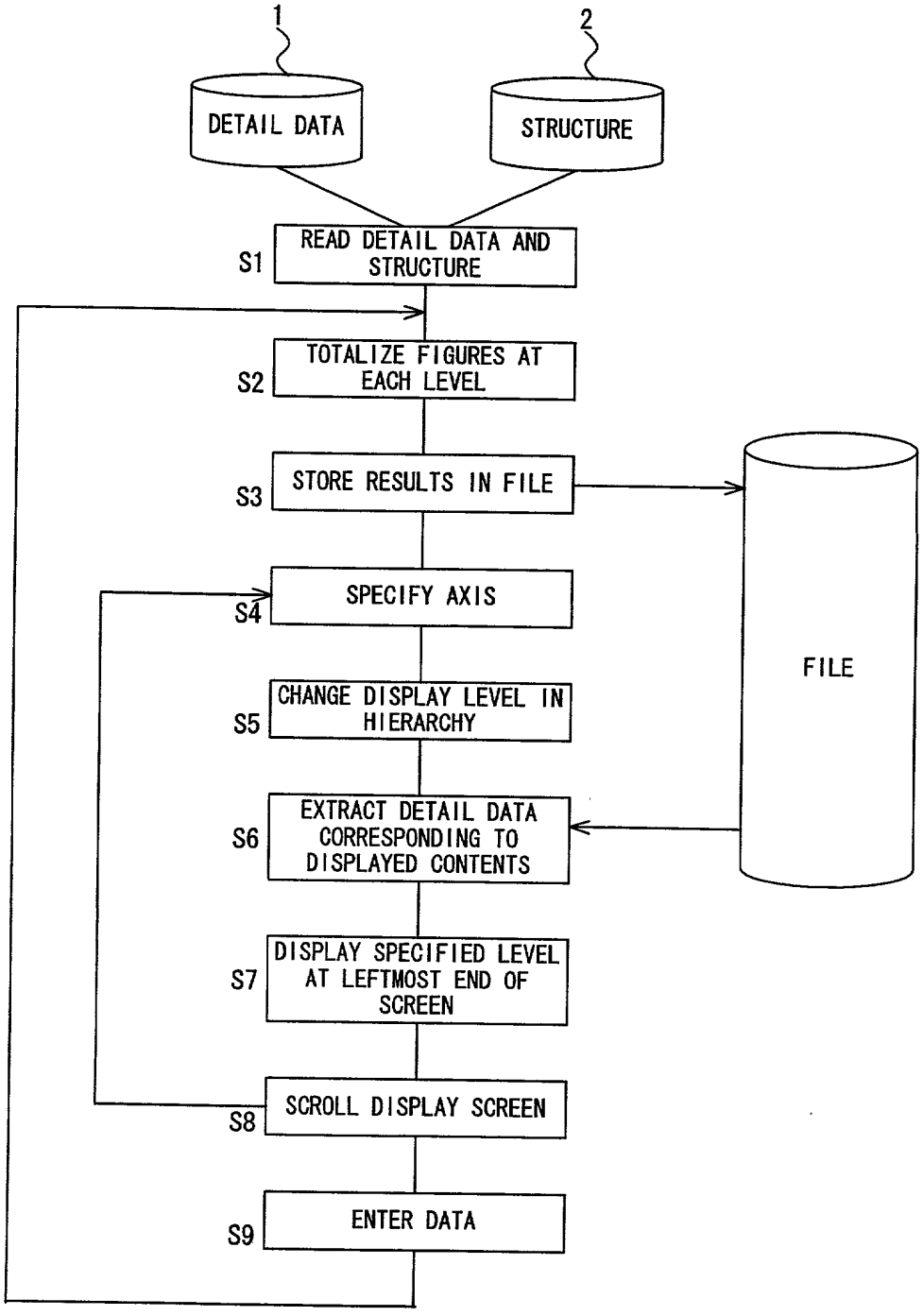

1
2
DETAIL DATA
STRUCTURE
S1
READ DETAIL DATA AND STRUCTURE
S2
TOTALIZE FIGURES AT EACH LEVEL
S3
STORE RESULTS IN FILE
FILE
S4
SPECIFY AXIS
S5
CHANGE DISPLAY LEVEL IN HIERARCHY
S6
EXTRACT DETAIL DATA CORRESPONDING TO DISPLAYED CONTENTS
S7
DISPLAY SPECIFIED LEVEL AT LEFTMOST END OF SCREEN
S8
SCROLL DISPLAY SCREEN
S9
ENTER DATA

FIG. 3

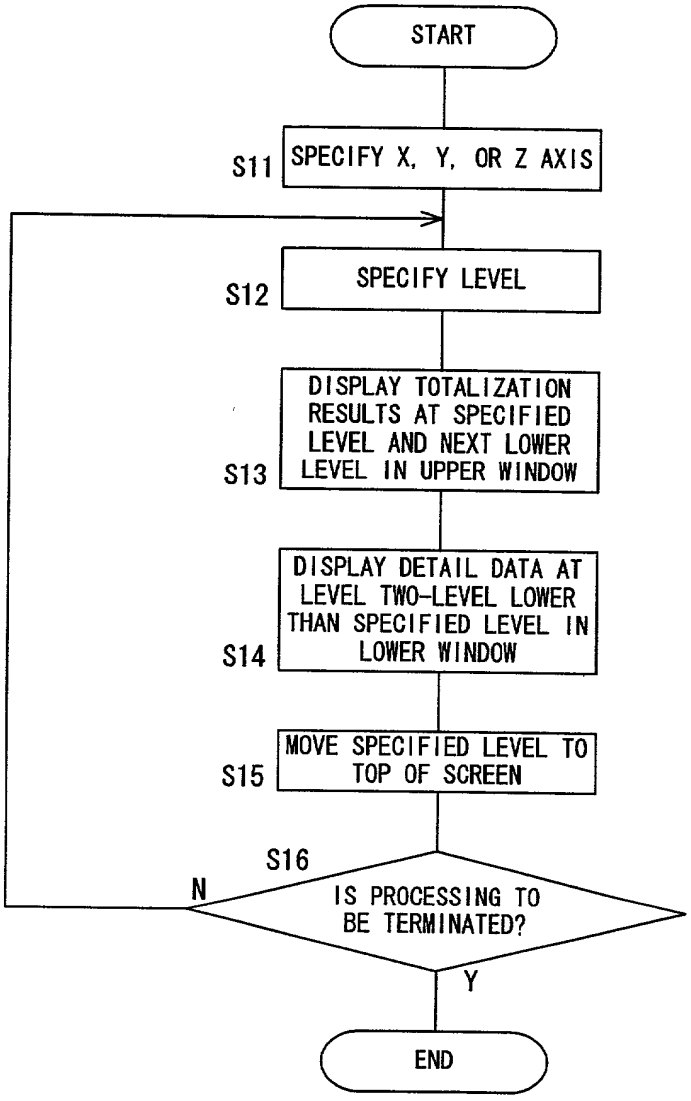

START
S11
SPECIFY X, Y, OR Z AXIS
S12
SPECIFY LEVEL
S13
DISPLAY TOTALIZATION RESULTS AT SPECIFIED LEVEL AND NEXT LOWER LEVEL IN UPPER WINDOW
S14
DISPLAY DETAIL DATA AT LEVEL TWO-LEVEL LOWER THAN SPECIFIED LEVEL IN LOWER WINDOW
S15
MOVE SPECIFIED LEVEL TO TOP OF SCREEN
S16
IS PROCESSING TO BE TERMINATED?
N
Y
END

FIG. 4

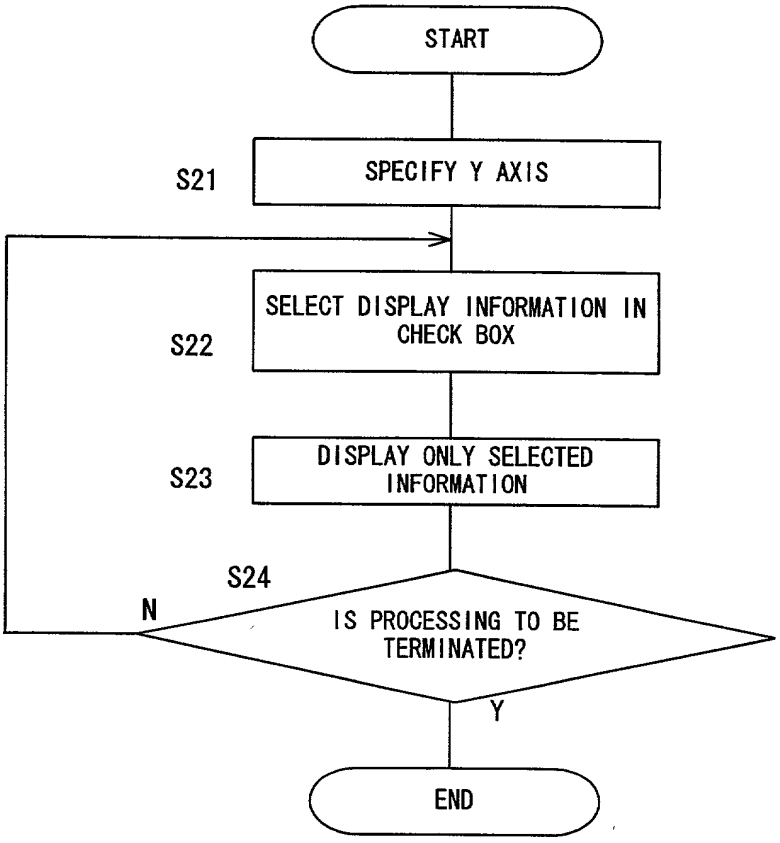

START
S21
SPECIFY Y AXIS
SELECT DISPLAY INFORMATION IN CHECK BOX
S22
DISPLAY ONLY SELECTED INFORMATION
S23
S24
N
IS PROCESSING TO BE TERMINATED?
Y
END

FIG. 5

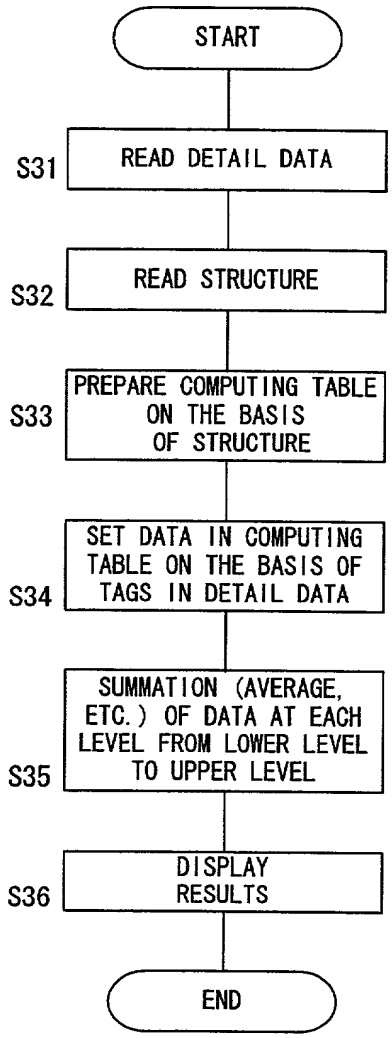

START
S31
READ DETAIL DATA
S32
READ STRUCTURE
S33
PREPARE COMPUTING TABLE ON THE BASIS OF STRUCTURE
S34
SET DATA IN COMPUTING TABLE ON THE BASIS OF TAGS IN DETAIL DATA
S35
SUMMATION (AVERAGE, ETC.) OF DATA AT EACH LEVEL FROM LOWER LEVEL TO UPPER LEVEL
S36
DISPLAY RESULTS
END

FIG. 6

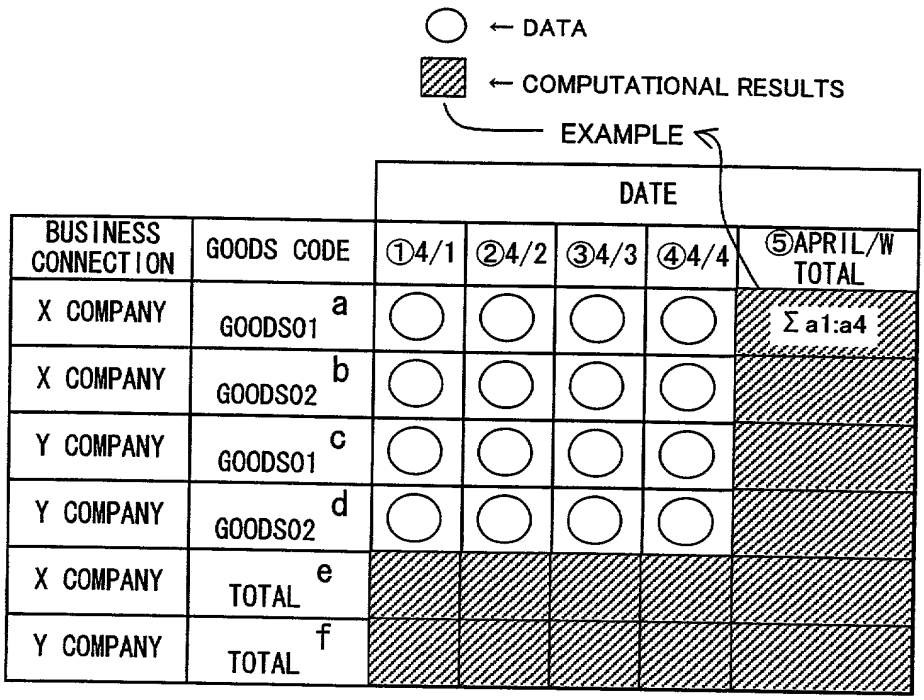
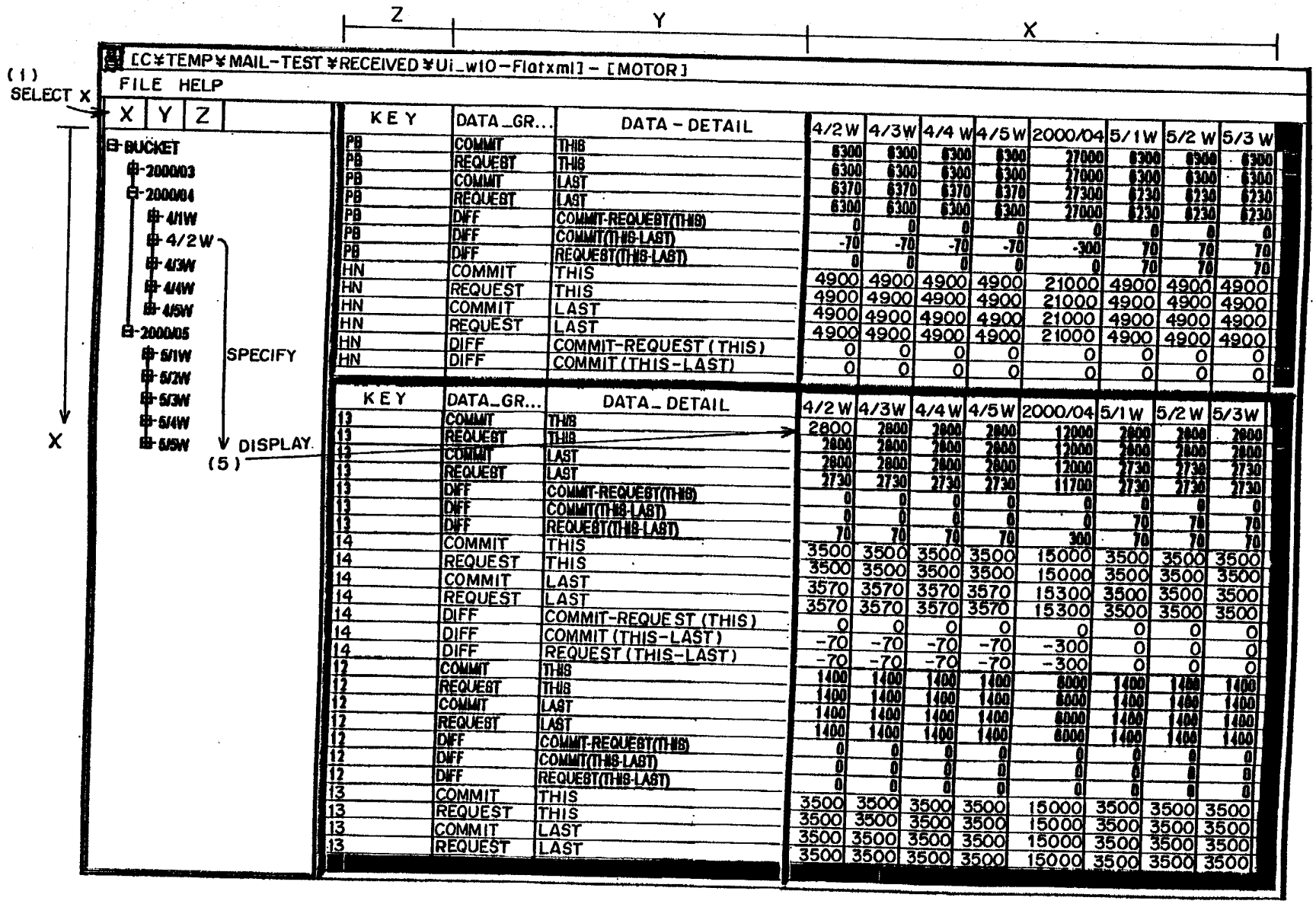

← DATA
← COMPUTATIONAL RESULTS
EXAMPLE
DATE
BUSINESS CONNECTION
GOODS CODE
①4/1
②4/2
③4/3
④4/4
⑤APRIL/W TOTAL
X COMPANY
GOODS01 a
Σ a1:a4
X COMPANY
GOODS02 b
Y COMPANY
GOODS01 c
Y COMPANY
GOODS02 d
X COMPANY
TOTAL e
Y COMPANY
TOTAL f

FIG. 7

FOT320 T6888760




| KEY | DATA_GR... | DATA - DETAIL | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W | 5/2W | 5/3W |
|---|---|---|---|---|---|---|---|---|---|---|
| PB | COMMIT | THIS | 6300 | 6300 | 6300 | 6300 | 27000 | 6300 | 6300 | 6300 |
| PB | REQUEST | THIS | 6300 | 6300 | 6300 | 6300 | 27000 | 6300 | 6300 | 6300 |
| PB | COMMIT | LAST | 6370 | 6370 | 6370 | 6370 | 27300 | 6230 | 6230 | 6230 |
| PB | REQUEST | LAST | 6300 | 6300 | 6300 | 6300 | 27000 | 6230 | 6230 | 6230 |
| PB | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| PB | DIFF | COMMIT(THIS-LAST) | -70 | -70 | -70 | -70 | -300 | 70 | 70 | 70 |
| PB | DIFF | REQUEST(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 70 | 70 | 70 |
| HN | COMMIT | THIS | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 | 4900 | 4900 |
| HN | REQUEST | THIS | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 | 4900 | 4900 |
| HN | COMMIT | LAST | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 | 4900 | 4900 |
| HN | REQUEST | LAST | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 | 4900 | 4900 |
| HN | DIFF | COMMIT-REQUEST (THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| HN | DIFF | COMMIT (THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

| KEY | DATA_GR... | DATA_DETAIL | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W | 5/2W | 5/3W |
|---|---|---|---|---|---|---|---|---|---|---|
| 13 | COMMIT | THIS | 2800 | 2800 | 2800 | 2800 | 12000 | 2800 | 2800 | 2800 |
| 13 | REQUEST | THIS | 2800 | 2800 | 2800 | 2800 | 12000 | 2800 | 2800 | 2800 |
| 13 | COMMIT | LAST | 2800 | 2800 | 2800 | 2800 | 12000 | 2730 | 2730 | 2730 |
| 13 | REQUEST | LAST | 2730 | 2730 | 2730 | 2730 | 11700 | 2730 | 2730 | 2730 |
| 13 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 13 | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 70 | 70 | 70 |
| 13 | DIFF | REQUEST(THIS-LAST) | 70 | 70 | 70 | 70 | 300 | 70 | 70 | 70 |
| 14 | COMMIT | THIS | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |
| 14 | REQUEST | THIS | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |
| 14 | COMMIT | LAST | 3570 | 3570 | 3570 | 3570 | 15300 | 3500 | 3500 | 3500 |
| 14 | REQUEST | LAST | 3570 | 3570 | 3570 | 3570 | 15300 | 3500 | 3500 | 3500 |
| 14 | DIFF | COMMIT-REQUEST (THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 14 | DIFF | COMMIT (THIS-LAST) | -70 | -70 | -70 | -70 | -300 | 0 | 0 | 0 |
| 14 | DIFF | REQUEST (THIS-LAST) | -70 | -70 | -70 | -70 | -300 | 0 | 0 | 0 |
| 12 | COMMIT | THIS | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 | 1400 | 1400 |
| 12 | REQUEST | THIS | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 | 1400 | 1400 |
| 12 | COMMIT | LAST | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 | 1400 | 1400 |
| 12 | REQUEST | LAST | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 | 1400 | 1400 |
| 12 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 12 | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 12 | DIFF | REQUEST(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 13 | COMMIT | THIS | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |
| 13 | REQUEST | THIS | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |
| 13 | COMMIT | LAST | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |
| 13 | REQUEST | LAST | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 | 3500 | 3500 |

FIG. 8

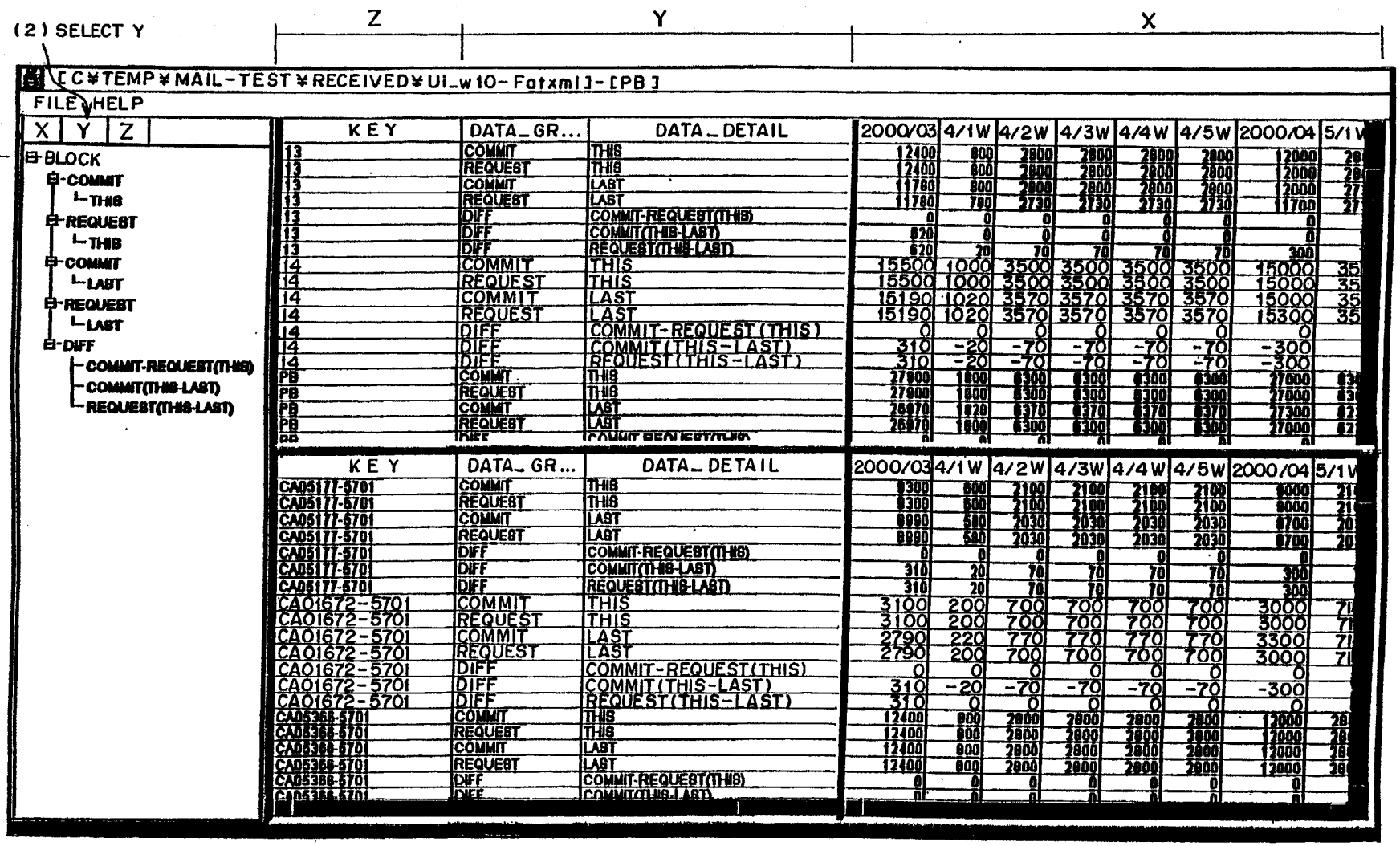
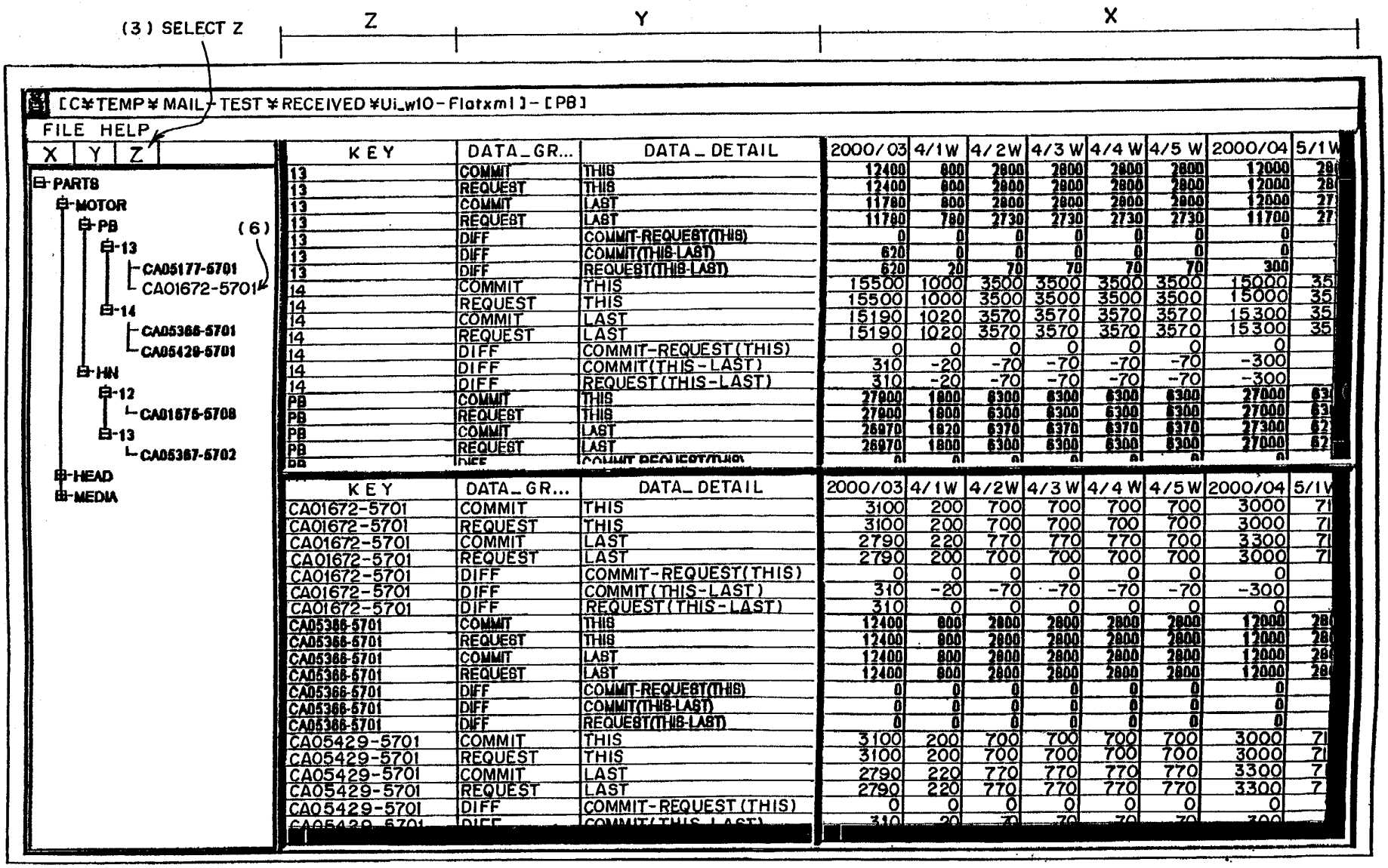

(2) SELECT Y
Z
Y
X
C:¥TEMP¥MAIL-TEST¥RECEIVED¥Ui_w10-Fatxml]-[PB]
FILE HELP
X Y Z
BLOCK
COMMIT
THIS
REQUEST
THIS
COMMIT
LAST
REQUEST
LAST
DIFF
COMMIT-REQUEST(THIS)
COMMIT(THIS-LAST)
REQUEST(THIS-LAST)
Y
KEY DATA_GR... DATA_DETAIL 2000/03 4/1W 4/2W 4/3W 4/4W 4/5W 2000/04 5/1W
13 COMMIT THIS 12400 800 2800 2800 2800 2800 12000
13 REQUEST THIS 12400 800 2800 2800 2800 2800 12000
13 COMMIT LAST 11780 800 2800 2800 2800 2800 12000
13 REQUEST LAST 11780 780 2730 2730 2730 2730 11700
13 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0
13 DIFF COMMIT(THIS-LAST) 620 0 0 0 0 0 0
13 DIFF REQUEST(THIS-LAST) 620 20 70 70 70 70 300
14 COMMIT THIS 15500 1000 3500 3500 3500 3500 15000
14 REQUEST THIS 15500 1000 3500 3500 3500 3500 15000
14 COMMIT LAST 15190 1020 3570 3570 3570 3570 15000
14 REQUEST LAST 15190 1020 3570 3570 3570 3570 15300
14 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0
14 DIFF COMMIT(THIS-LAST) 310 -20 -70 -70 -70 -70 -300
14 DIFF REQUEST(THIS-LAST) 310 -20 -70 -70 -70 -70 -300
PB COMMIT THIS 27900 1800 6300 6300 6300 6300 27000
PB REQUEST THIS 27900 1800 6300 6300 6300 6300 27000
PB COMMIT LAST 26970 1820 6370 6370 6370 6370 27300
PB REQUEST LAST 26970 1800 6300 6300 6300 6300 27000
KEY DATA_GR... DATA_DETAIL 2000/03 4/1W 4/2W 4/3W 4/4W 4/5W 2000/04 5/1W
CA05177-5701 COMMIT THIS 8300 800 2100 2100 2100 2100 8000
CA05177-5701 REQUEST THIS 8300 800 2100 2100 2100 2100 8000
CA05177-5701 COMMIT LAST 8990 580 2030 2030 2030 2030 8700
CA05177-5701 REQUEST LAST 8990 580 2030 2030 2030 2030 8700
CA05177-5701 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0
CA05177-5701 DIFF COMMIT(THIS-LAST) 310 20 70 70 70 70 300
CA05177-5701 DIFF REQUEST(THIS-LAST) 310 20 70 70 70 70 300
CA01672-5701 COMMIT THIS 3100 200 700 700 700 700 3000
CA01672-5701 REQUEST THIS 3100 200 700 700 700 700 3000
CA01672-5701 COMMIT LAST 2790 220 770 770 770 770 3300
CA01672-5701 REQUEST LAST 2790 200 700 700 700 700 3000
CA01672-5701 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0
CA01672-5701 DIFF COMMIT(THIS-LAST) 310 -20 -70 -70 -70 -70 -300
CA01672-5701 DIFF REQUEST(THIS-LAST) 310 0 0 0 0 0 0
CA05388-5701 COMMIT THIS 12400 800 2800 2800 2800 2800 12000
CA05388-5701 REQUEST THIS 12400 800 2800 2800 2800 2800 12000
CA05388-5701 COMMIT LAST 12400 800 2800 2800 2800 2800 12000
CA05388-5701 REQUEST LAST 12400 800 2800 2800 2800 2800 12000
CA05388-5701 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0

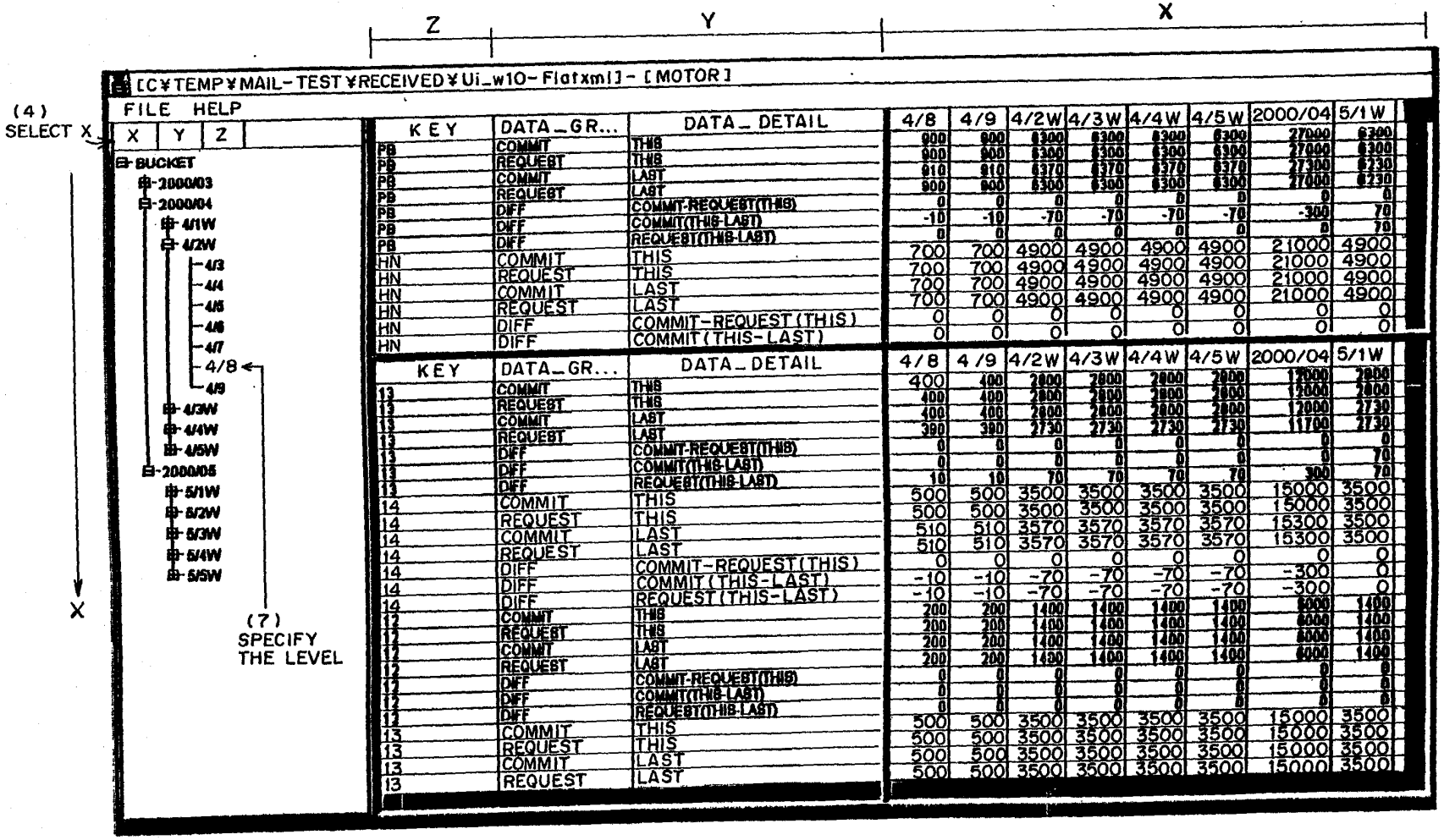
FIG. 9




| KEY | DATA_GR... | DATA_DETAIL | 2000/03 | 4/1W | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W |
|---|---|---|---|---|---|---|---|---|---|---|
| 13 | COMMIT | THIS | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| 13 | REQUEST | THIS | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| 13 | COMMIT | LAST | 11780 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 27 |
| 13 | REQUEST | LAST | 11780 | 780 | 2730 | 2730 | 2730 | 2730 | 11700 | 27 |
| 13 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| 13 | DIFF | COMMIT(THIS-LAST) | 620 | 0 | 0 | 0 | 0 | 0 | 0 | |
| 13 | DIFF | REQUEST(THIS-LAST) | 620 | 20 | 70 | 70 | 70 | 70 | 300 | |
| 14 | COMMIT | THIS | 15500 | 1000 | 3500 | 3500 | 3500 | 3500 | 15000 | 35 |
| 14 | REQUEST | THIS | 15500 | 1000 | 3500 | 3500 | 3500 | 3500 | 15000 | 35 |
| 14 | COMMIT | LAST | 15190 | 1020 | 3570 | 3570 | 3570 | 3570 | 15300 | 35 |
| 14 | REQUEST | LAST | 15190 | 1020 | 3570 | 3570 | 3570 | 3570 | 15300 | 35 |
| 14 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| 14 | DIFF | COMMIT(THIS-LAST) | 310 | -20 | -70 | -70 | -70 | -70 | -300 | |
| 14 | DIFF | REQUEST(THIS-LAST) | 310 | -20 | -70 | -70 | -70 | -70 | -300 | |
| PB | COMMIT | THIS | 27900 | 1800 | 6300 | 6300 | 6300 | 6300 | 27000 | 63 |
| PB | REQUEST | THIS | 27900 | 1800 | 6300 | 6300 | 6300 | 6300 | 27000 | 63 |
| PB | COMMIT | LAST | 26970 | 1820 | 6370 | 6370 | 6370 | 6370 | 27300 | 62 |
| PB | REQUEST | LAST | 26970 | 1800 | 6300 | 6300 | 6300 | 6300 | 27000 | 62 |
| PB | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |

| KEY | DATA_GR... | DATA_DETAIL | 2000/03 | 4/1W | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W |
|---|---|---|---|---|---|---|---|---|---|---|
| CA01672-5701 | COMMIT | THIS | 3100 | 200 | 700 | 700 | 700 | 700 | 3000 | 71 |
| CA01672-5701 | REQUEST | THIS | 3100 | 200 | 700 | 700 | 700 | 700 | 3000 | 71 |
| CA01672-5701 | COMMIT | LAST | 2790 | 220 | 770 | 770 | 770 | 700 | 3300 | 71 |
| CA01672-5701 | REQUEST | LAST | 2790 | 200 | 700 | 700 | 700 | 700 | 3000 | 71 |
| CA01672-5701 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA01672-5701 | DIFF | COMMIT(THIS-LAST) | 310 | -20 | -70 | -70 | -70 | -70 | -300 | |
| CA01672-5701 | DIFF | REQUEST(THIS-LAST) | 310 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA05368-5701 | COMMIT | THIS | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| CA05368-5701 | REQUEST | THIS | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| CA05368-5701 | COMMIT | LAST | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| CA05368-5701 | REQUEST | LAST | 12400 | 800 | 2800 | 2800 | 2800 | 2800 | 12000 | 28 |
| CA05368-5701 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA05368-5701 | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA05368-5701 | DIFF | REQUEST(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA05429-5701 | COMMIT | THIS | 3100 | 200 | 700 | 700 | 700 | 700 | 3000 | 71 |
| CA05429-5701 | REQUEST | THIS | 3100 | 200 | 700 | 700 | 700 | 700 | 3000 | 71 |
| CA05429-5701 | COMMIT | LAST | 2790 | 220 | 770 | 770 | 770 | 770 | 3300 | 71 |
| CA05429-5701 | REQUEST | LAST | 2790 | 220 | 770 | 770 | 770 | 770 | 3300 | 7 |
| CA05429-5701 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| CA05429-5701 | DIFF | COMMIT(THIS-LAST) | 310 | 20 | 70 | 70 | 70 | 70 | 300 | |

FIG. 10




| KEY | DATA_GR... | DATA_DETAIL | 4/8 | 4/9 | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W |
|---|---|---|---|---|---|---|---|---|---|---|
| PB | COMMIT | THIS | 900 | 900 | 6300 | 6300 | 6300 | 6300 | 27000 | 6300 |
| PB | REQUEST | THIS | 900 | 900 | 6300 | 6300 | 6300 | 6300 | 27000 | 6300 |
| PB | COMMIT | LAST | 910 | 910 | 6370 | 6370 | 6370 | 6370 | 27300 | 6230 |
| PB | REQUEST | LAST | 900 | 900 | 6300 | 6300 | 6300 | 6300 | 27000 | 6230 |
| PB | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| PB | DIFF | COMMIT(THIS-LAST) | -10 | -10 | -70 | -70 | -70 | -70 | -300 | 70 |
| PB | DIFF | REQUEST(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 70 |
| HN | COMMIT | THIS | 700 | 700 | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 |
| HN | REQUEST | THIS | 700 | 700 | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 |
| HN | COMMIT | LAST | 700 | 700 | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 |
| HN | REQUEST | LAST | 700 | 700 | 4900 | 4900 | 4900 | 4900 | 21000 | 4900 |
| HN | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| HN | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

| KEY | DATA_GR... | DATA_DETAIL | 4/8 | 4/9 | 4/2W | 4/3W | 4/4W | 4/5W | 2000/04 | 5/1W |
|---|---|---|---|---|---|---|---|---|---|---|
| 13 | COMMIT | THIS | 400 | 400 | 2800 | 2800 | 2800 | 2800 | 12000 | 2800 |
| 13 | REQUEST | THIS | 400 | 400 | 2800 | 2800 | 2800 | 2800 | 12000 | 2800 |
| 13 | COMMIT | LAST | 400 | 400 | 2800 | 2800 | 2800 | 2800 | 12000 | 2730 |
| 13 | REQUEST | LAST | 390 | 390 | 2730 | 2730 | 2730 | 2730 | 11700 | 2730 |
| 13 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 13 | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 70 |
| 13 | DIFF | REQUEST(THIS-LAST) | 10 | 10 | 70 | 70 | 70 | 70 | 300 | 70 |
| 14 | COMMIT | THIS | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |
| 14 | REQUEST | THIS | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |
| 14 | COMMIT | LAST | 510 | 510 | 3570 | 3570 | 3570 | 3570 | 15300 | 3500 |
| 14 | REQUEST | LAST | 510 | 510 | 3570 | 3570 | 3570 | 3570 | 15300 | 3500 |
| 14 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 14 | DIFF | COMMIT(THIS-LAST) | -10 | -10 | -70 | -70 | -70 | -70 | -300 | 0 |
| 14 | DIFF | REQUEST(THIS-LAST) | -10 | -10 | -70 | -70 | -70 | -70 | -300 | 0 |
| 12 | COMMIT | THIS | 200 | 200 | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 |
| 12 | REQUEST | THIS | 200 | 200 | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 |
| 12 | COMMIT | LAST | 200 | 200 | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 |
| 12 | REQUEST | LAST | 200 | 200 | 1400 | 1400 | 1400 | 1400 | 6000 | 1400 |
| 12 | DIFF | COMMIT-REQUEST(THIS) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 12 | DIFF | COMMIT(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 12 | DIFF | REQUEST(THIS-LAST) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 13 | COMMIT | THIS | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |
| 13 | REQUEST | THIS | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |
| 13 | COMMIT | LAST | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |
| 13 | REQUEST | LAST | 500 | 500 | 3500 | 3500 | 3500 | 3500 | 15000 | 3500 |

FIG. 11

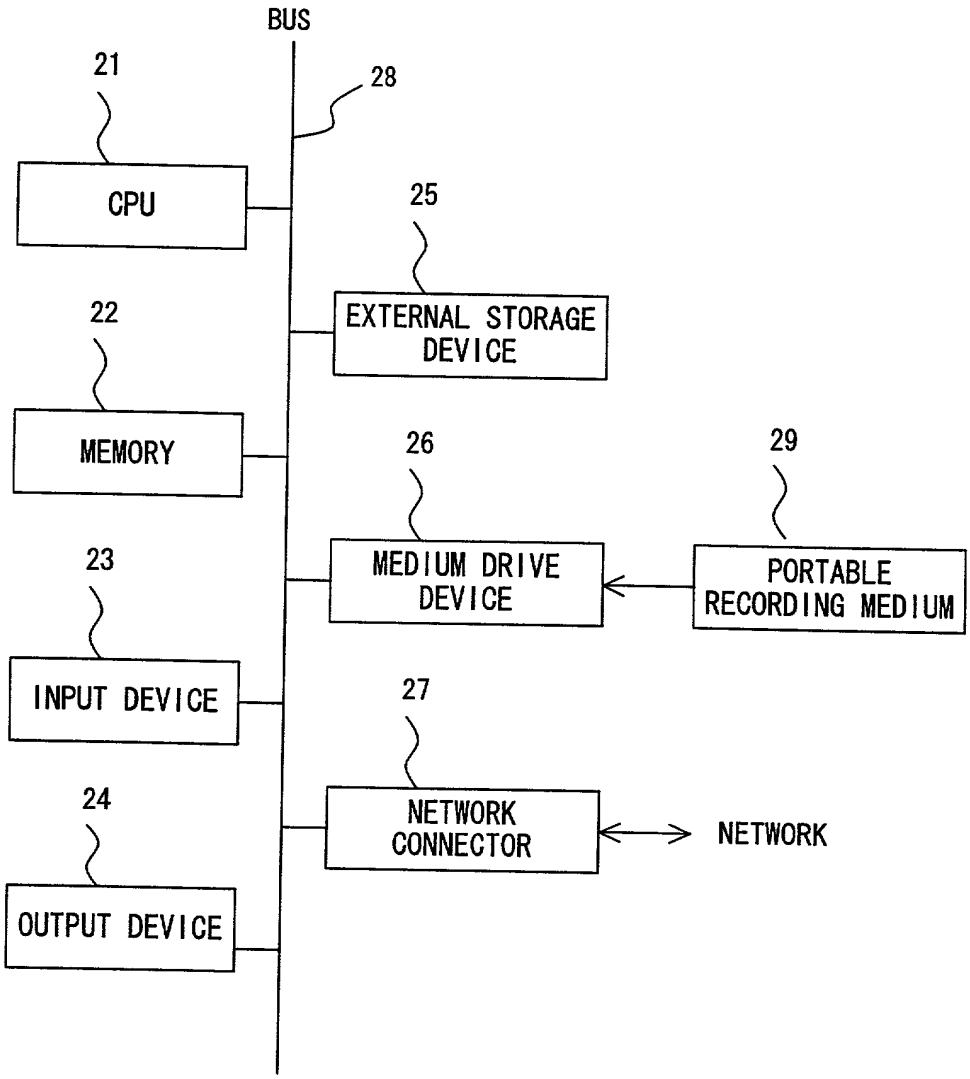

BUS
21
28
CPU
25
EXTERNAL STORAGE DEVICE
22
MEMORY
26
29
MEDIUM DRIVE DEVICE
PORTABLE RECORDING MEDIUM
23
INPUT DEVICE
27
NETWORK CONNECTOR
NETWORK
24
OUTPUT DEVICE

FIG. 12

JP2000-165960

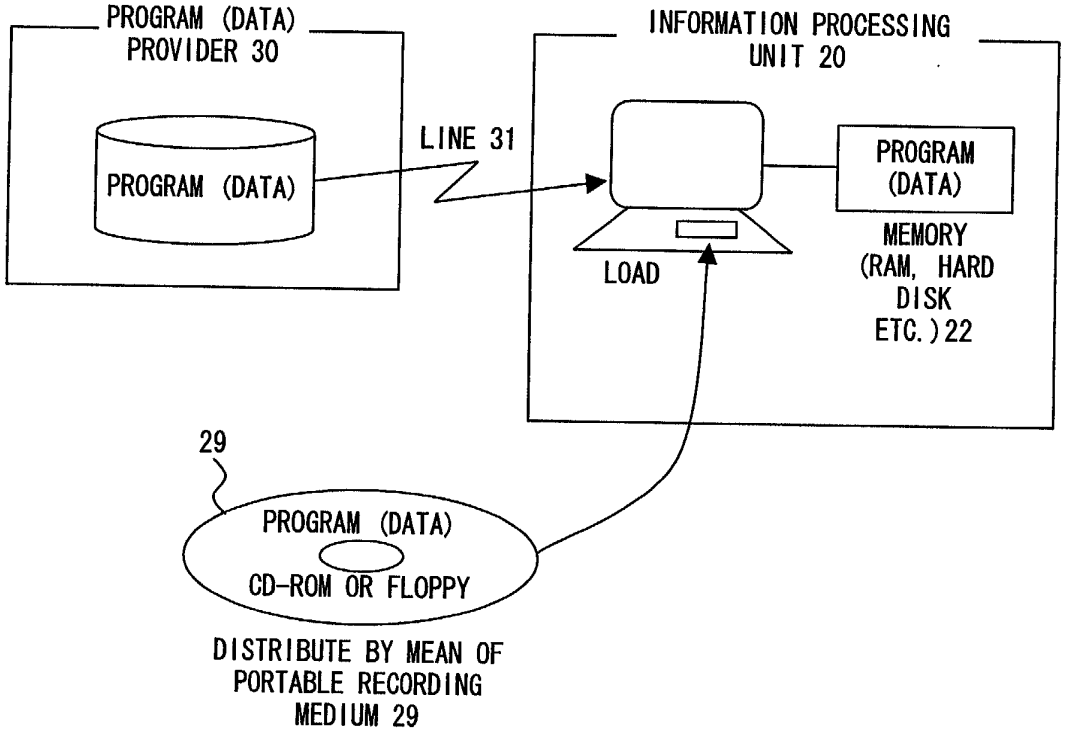



FIG. 13